

1/16

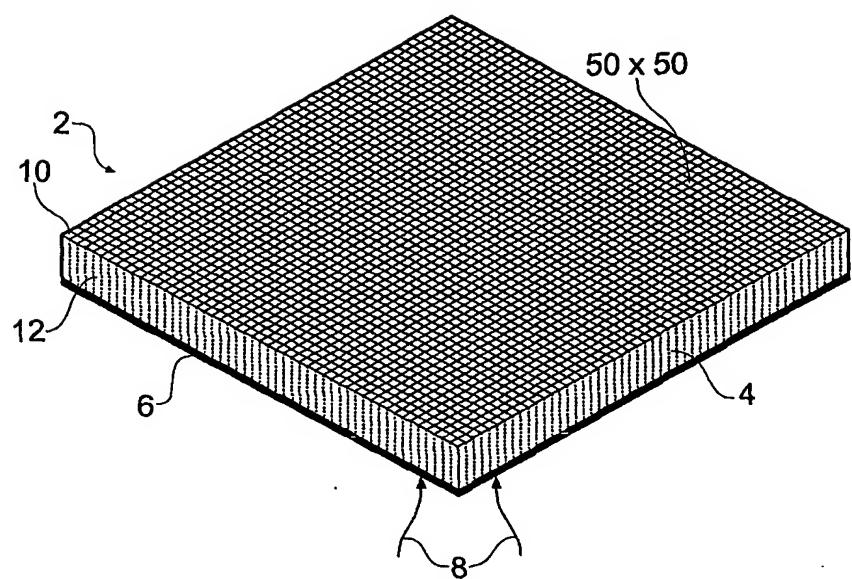


Fig. 1

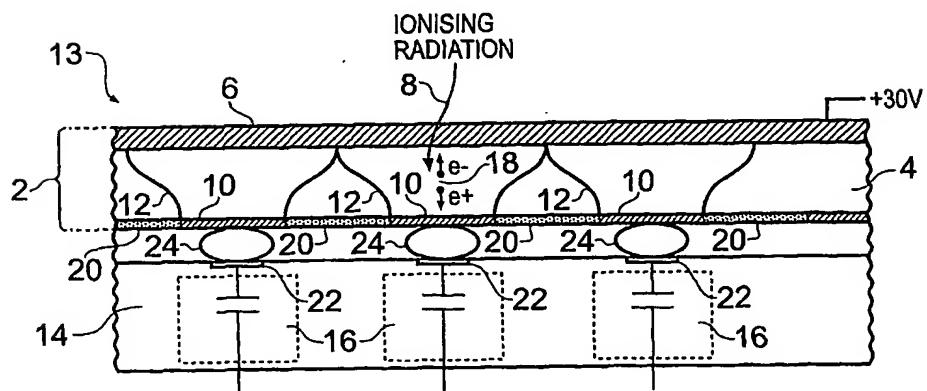


Fig. 2

2/16

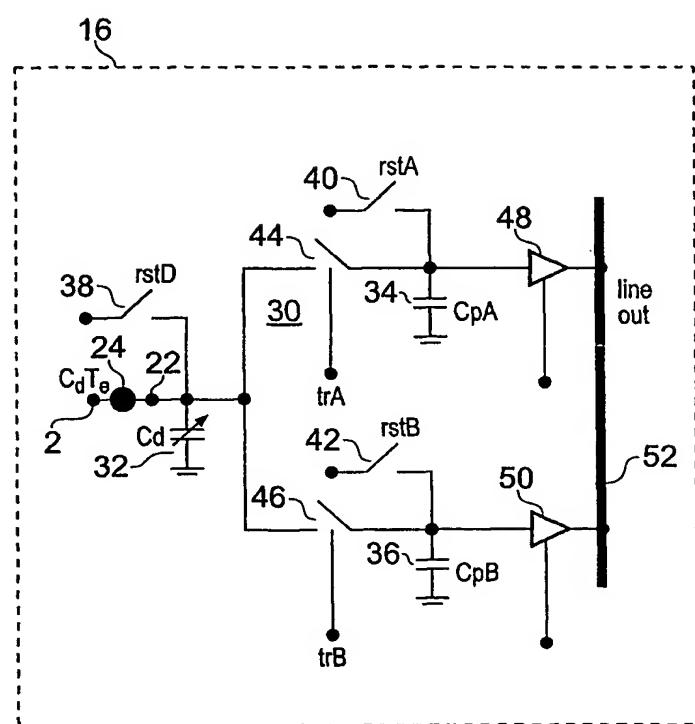


Fig. 3

3/16

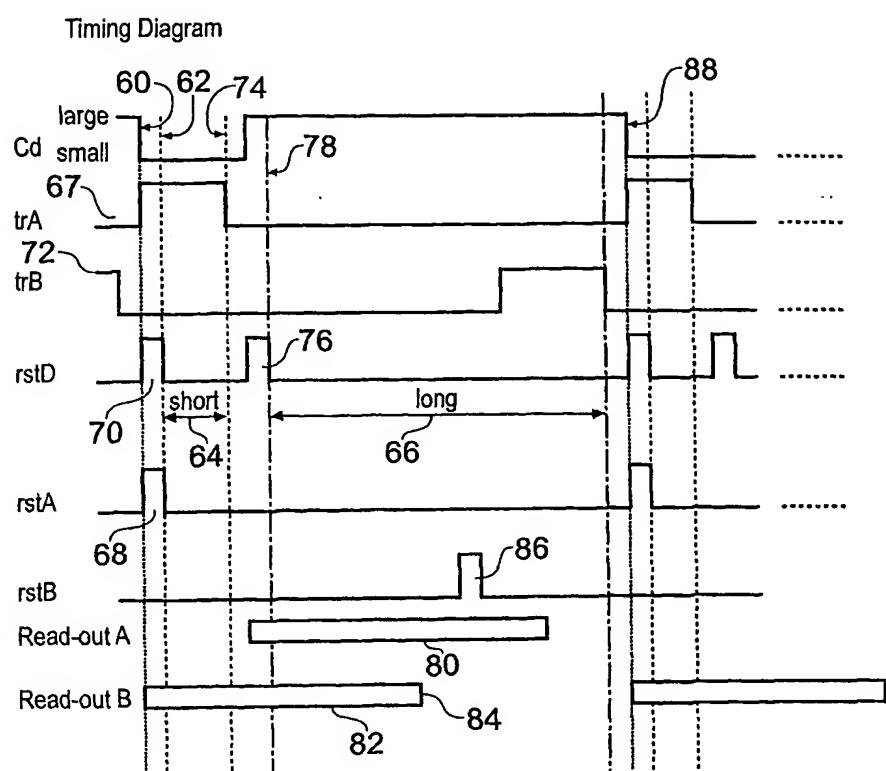


Fig. 4

4/16

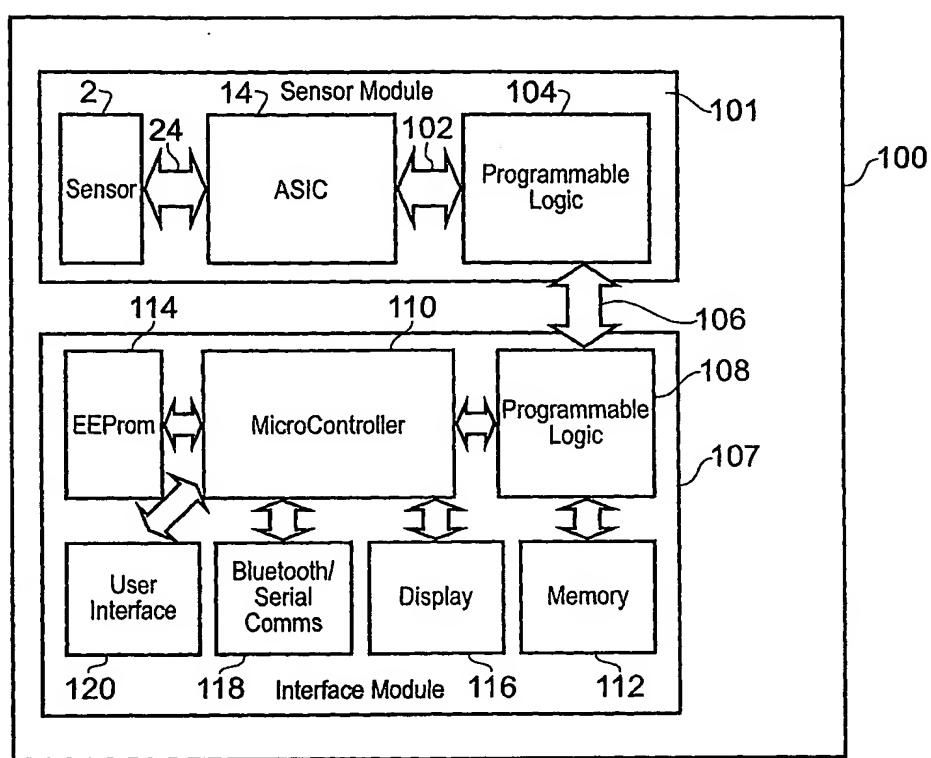


Fig. 5(a)

5/16

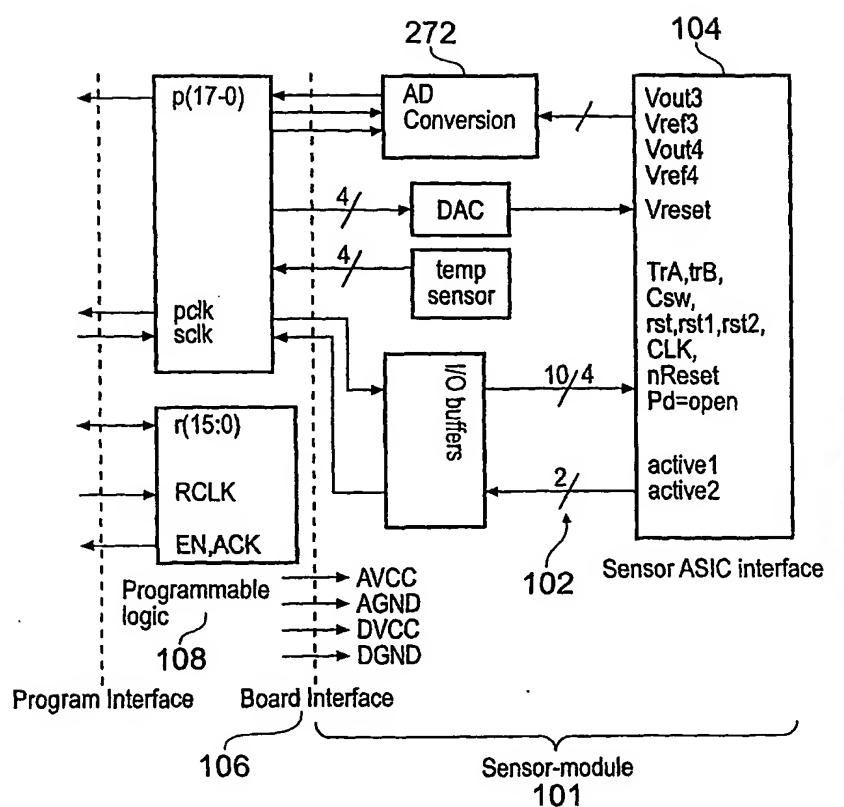


Fig. 5(b)

6/16

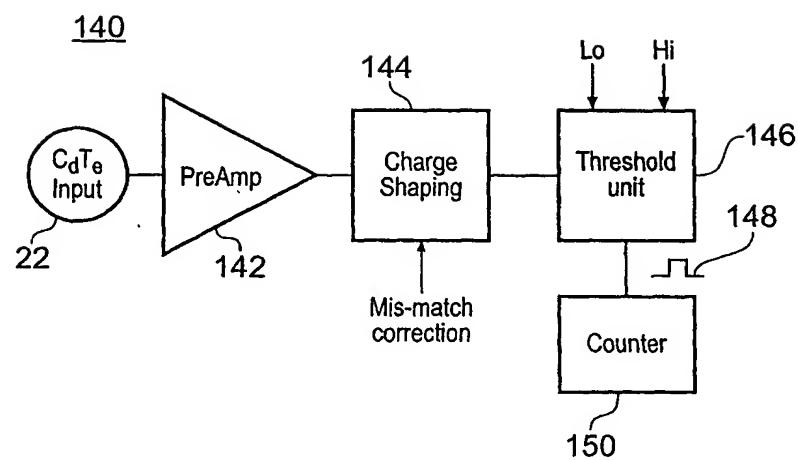


Fig. 6

7/16

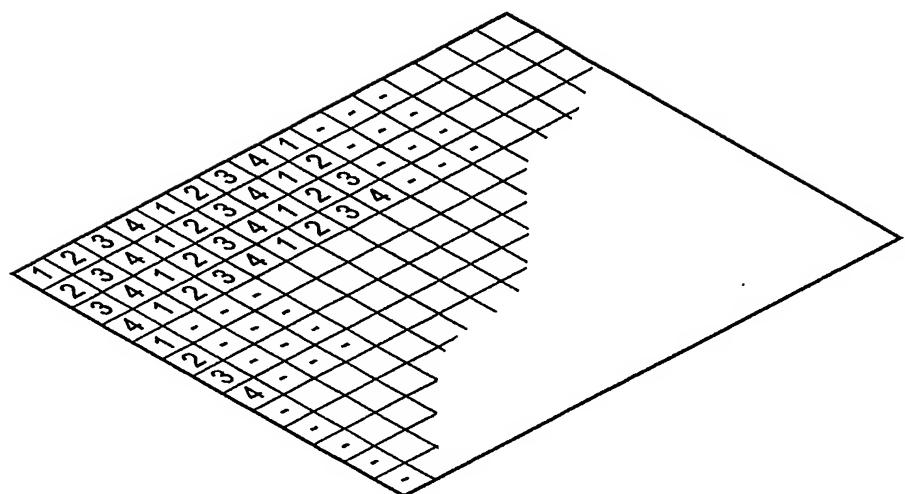


Fig. 7(a)

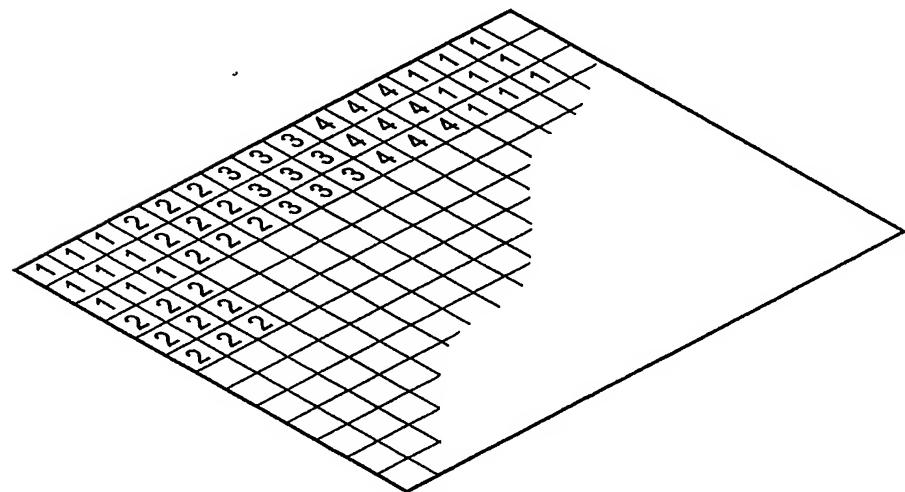


Fig. 7(b)

8/16

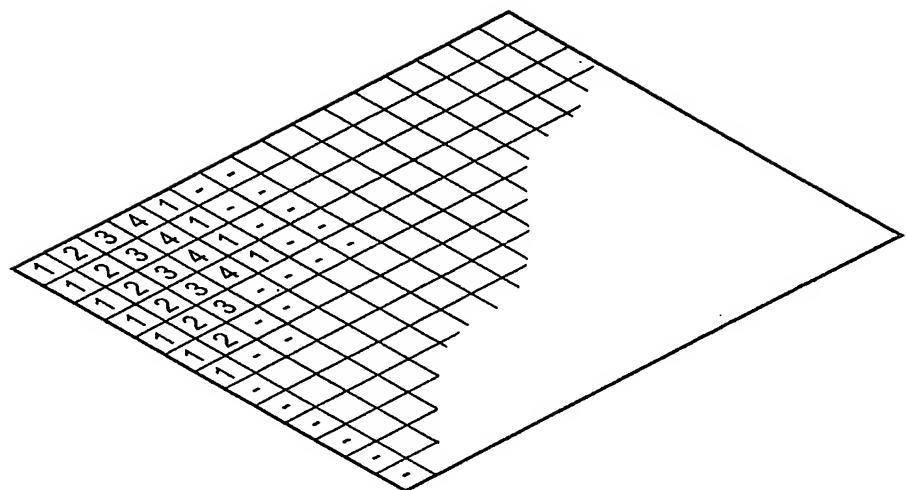
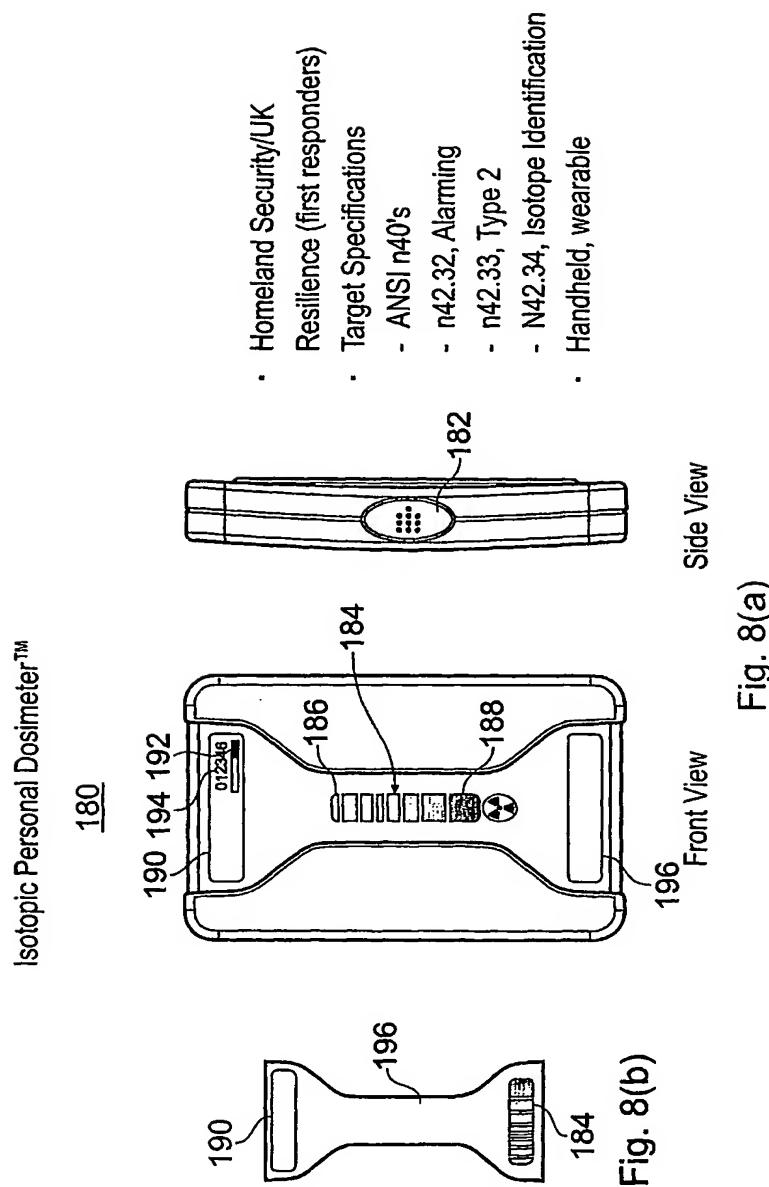
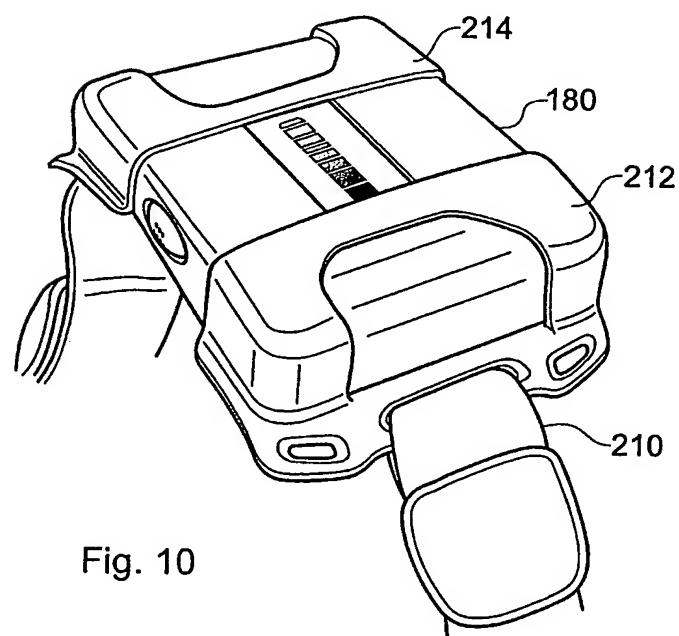
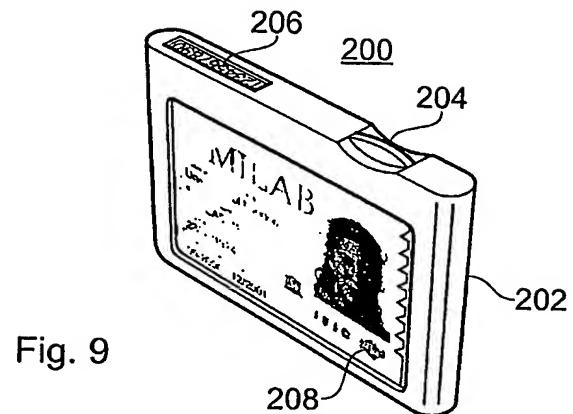


Fig. 7(c)

9/16



10/16



11/16

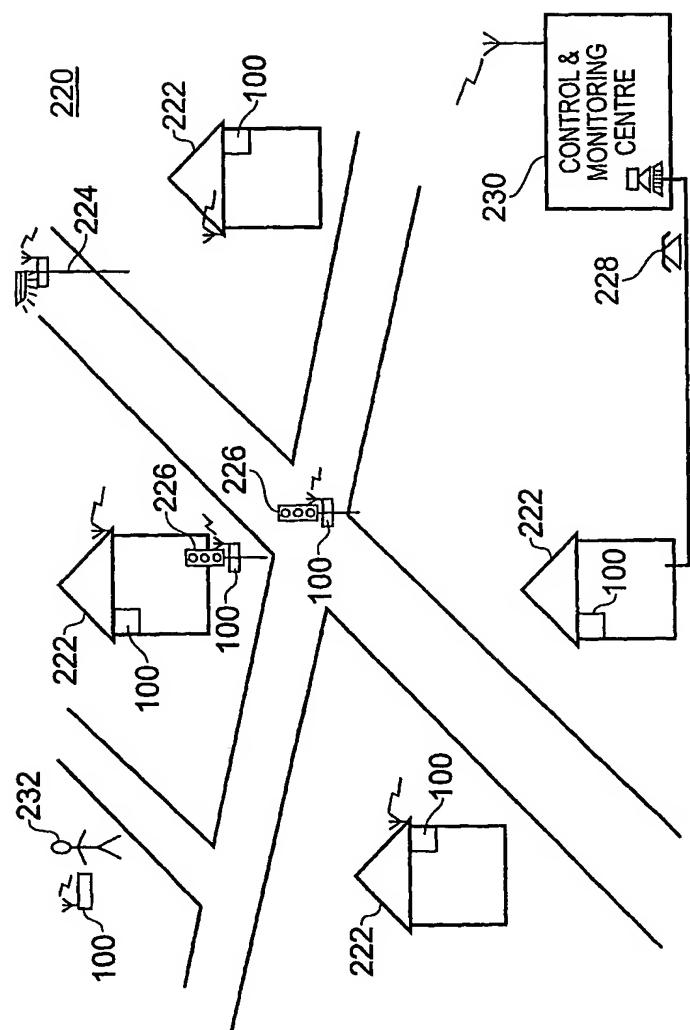


Fig. 11

12/16

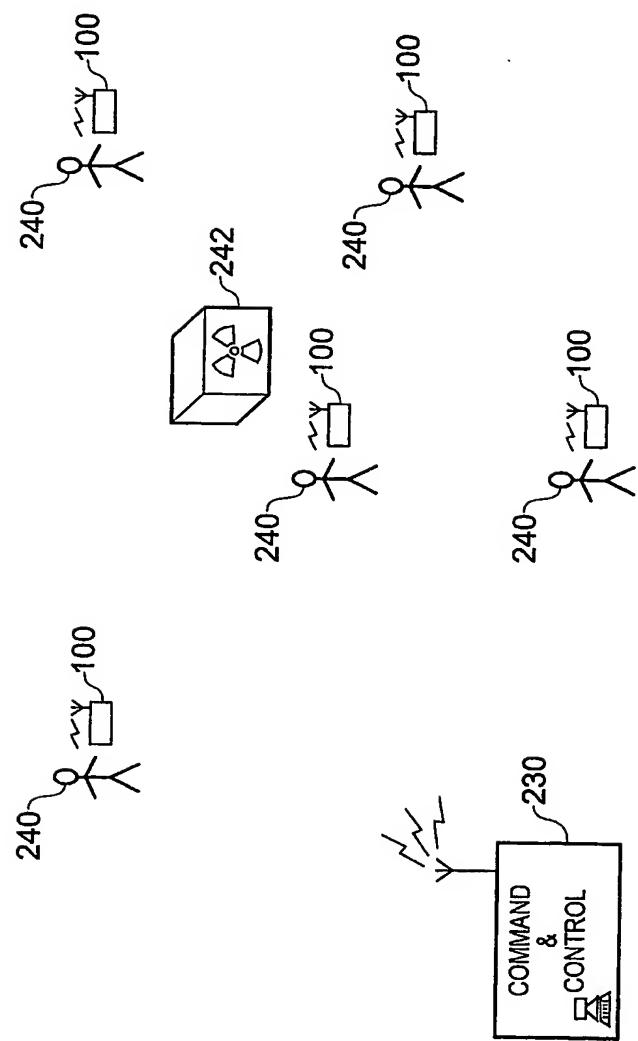


Fig. 12

13/16

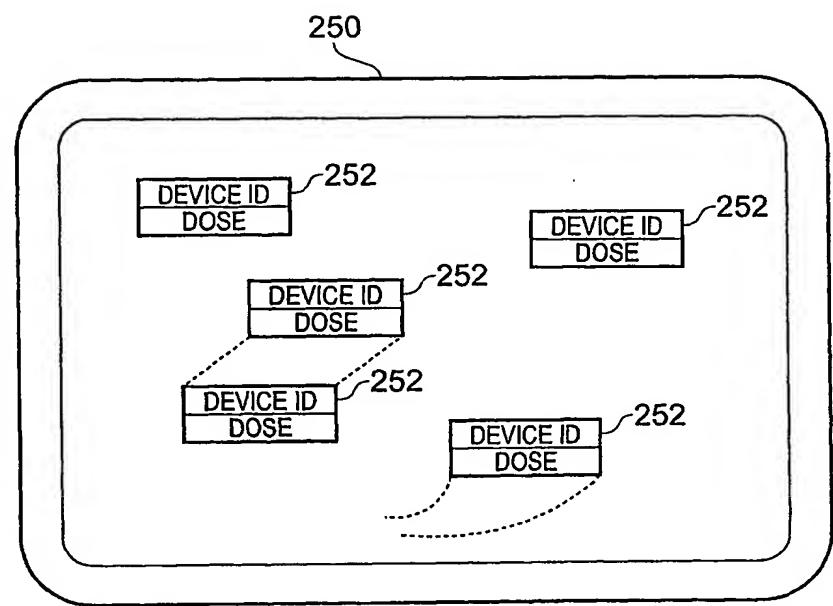


Fig. 13

14/16

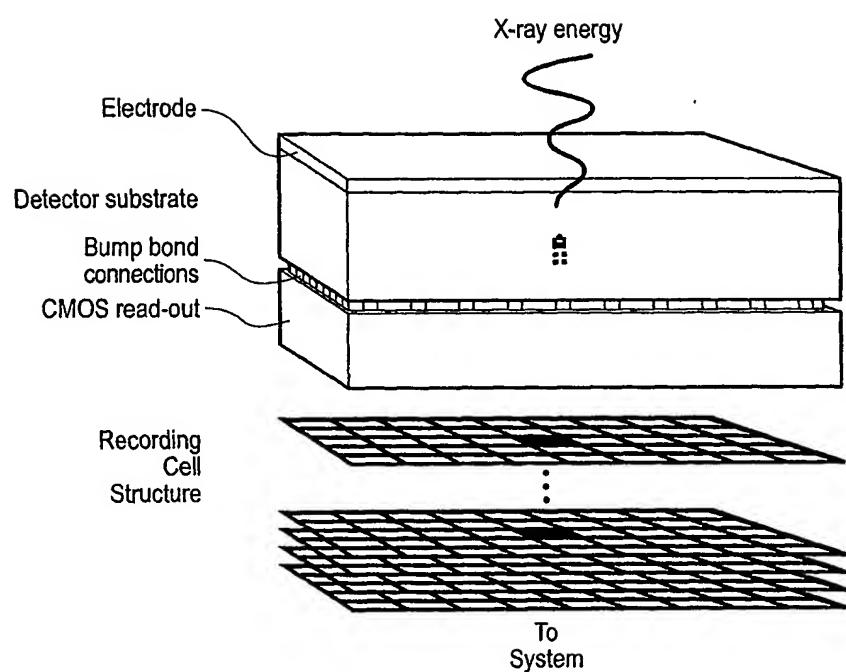


Fig. 14

Drawing exhibits the generic structure of the detector layer, its bonding to the CMOS recording layer and a diagrammatic representation of the cell structure

15/16

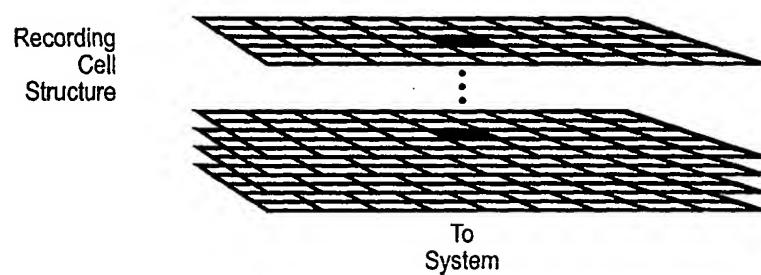


Fig. 15

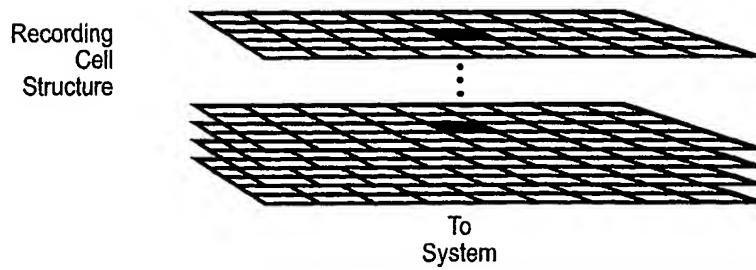


Fig. 16

16/16

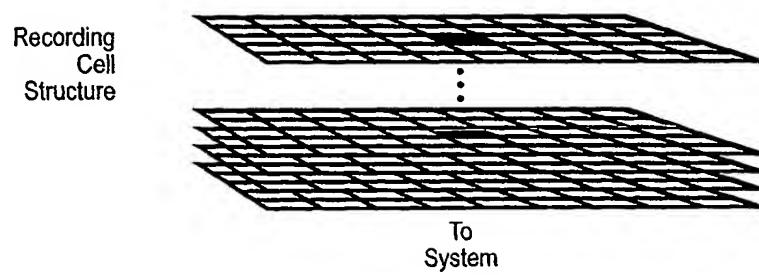


Fig. 17